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Circuits and Systems for Wireless CommunicationsAnalog and VLSI CircuitsThe VLSI HandbookLow-power HF MicroelectronicsSingle-chip Bluetooth SolutionsOn-Chip Electro-Static Discharge (ESD) Protection for Radio-Frequency Integrated Circuits1997 IEEE MTT-S Symposium on Technologies for Wireless ApplicationsLatchupElectronic DesignLateral Power Transistors in Integrated CircuitsIntegrated Circuits for Wireless CommunicationsIEEE Circuits & DevicesPortable DesignMicrowave JournalIEEE Transactions on Circuits and Systems2000 IEEE Radio Frequency Integrated Circuits (RFIC) SymposiumJ@pan, IncIEICE Transactions on Electronics2000 HD International Conference on High-Density Interconnect and Systems PackagingDissertation Abstracts International Markus Helfenstein Wai-Kai Chen Wai-Kai Chen Gerson A. S. Machado Sudeepto Chakraborty Qiang Cui Steven H. Voldman Tobias Erlbacher Asad A. Abidi IEEE Microwave Theory and Techniques Society

Circuits and Systems for Wireless Communications Analog and VLSI Circuits The VLSI Handbook Low-power HF Microelectronics Single-chip Bluetooth Solutions On-Chip Electro-Static Discharge (ESD) Protection for Radio-Frequency Integrated Circuits 1997 IEEE MTT-S Symposium on Technologies for Wireless Applications Latchup Electronic Design Lateral Power Transistors in Integrated Circuits Integrated Circuits for Wireless Communications IEEE Circuits & Devices Portable Design Microwave Journal IEEE Transactions on Circuits and Systems 2000 IEEE Radio Frequency Integrated Circuits (RFIC) Symposium J@pan, Inc IEICE Transactions on Electronics 2000 HD International Conference on High-Density Interconnect and Systems Packaging Dissertation Abstracts International *Markus Helfenstein Wai-Kai Chen Wai-Kai Chen Gerson A. S. Machado Sudeepto Chakraborty Qiang Cui Steven H. Voldman Tobias Erlbacher Asad A. Abidi IEEE Microwave Theory and Techniques Society*

part i rf system integration 1 rf system integration c toumazou 2 rf system board level integration for mobile phones g j aspin 3 integration of rf systems on a chip p j mole 4 towards the full integration of wireless front end circuits m steyaert 5 gsm transceiver front end circuits in 0 25 mum cmos q huang et al part ii rf front end circuits 6 rf front end circuits q huang 7 phase noise to carrier ratio in lc oscillators q huang 8 design study of a 900 mhz 1 8 ghz cmos transceiver for dual band applications b razavi 9 integrated wireless transc

featuring hundreds of illustrations and references this volume in the third edition of the circuits and filters handbook provides the latest information on analog and vlsi circuits omitting extensive theory and proofs in favor of numerous examples

throughout each chapter the first part of the text focuses on analog integrated circuits presenting up to date knowledge on monolithic device models analog circuit cells high performance analog circuits rf communication circuits and pll circuits in the second half of the book well known contributors offer the latest findings on vlsi circuits including digital systems data converters and systolic arrays

for the new millenium wai kai chen introduced a monumental reference for the design analysis and prediction of vlsi circuits the vlsi handbook still a valuable tool for dealing with the most dynamic field in engineering this second edition includes 13 sections comprising nearly 100 chapters focused on the key concepts models and equations written by a stellar international panel of expert contributors this handbook is a reliable comprehensive resource for real answers to practical problems it emphasizes fundamental theory underlying professional applications and also reflects key areas of industrial and research focus what s in the second edition sections on low power electronics and design vlsi signal processing chapters on cmos fabrication content addressable memory compound semiconductor rf circuits high speed circuit design principles sige hbt technology bipolar junction transistor amplifiers performance modeling and analysis using systemc design languages expanded from two chapters to twelve testing of digital systems structured for convenient navigation and loaded with practical solutions the vlsi handbook second edition remains the first choice for answers to the problems and challenges faced daily in engineering practice

this book brings together innovative modelling simulation and design techniques in cmos soi gaas and bjt to achieve successful high yield manufacture for low power high speed and reliable by design analogue and mixed mode integrated systems

this book enables readers to design effective esd protection solutions for all mainstream rf fabrication processes gaas phemt sige hbt cmos the new techniques introduced by the authors have much higher protection levels and much lower parasitic effects than those of existing esd protection devices the authors describe in detail the esd phenomenon as well as esd protection fundamentals standards test equipment and basic design strategies readers will benefit from realistic case studies of esd protection for rfics and will learn to increase significantly modern rfics esd safety level while maximizing rf performance

interest in latchup is being renewed with the evolution of complimentary metal oxide semiconductor cmos technology metal oxide semiconductor field effect transistor mosfet scaling and high level system on chip soc integration clear methodologies that grant protection from latchup with insight into the physics technology and circuit issues involved are in increasing demand this book describes cmos and bicmos semiconductor technology and their sensitivity to present day latchup phenomena from basic over voltage and over current conditions single event latchup sel and cable discharge events cde to latchup domino phenomena

it contains chapters focusing on bipolar physics latchup theory latchup and guard ring characterization structures characterization testing product level test systems product level testing and experimental results discussions on state of the art semiconductor processes design layout and circuit level and system level latchup solutions are also included as well as latchup semiconductor process solutions for both cmos to bicmos such as shallow trench deep trench retrograde wells connecting implants sub collectors heavily doped buried layers and buried grids from single to triple well cmos practical latchup design methods automated and bench level latchup testing methods and techniques latchup theory of logarithm resistance space generalized alpha a space beta b space new latchup design methods connecting the theoretical to the practical analysis and examples of latchup computer aided design cad methodologies from design rule checking drc and logical to physical design to new latchup cad methodologies that address latchup for internal and external latchup on a local as well as global design level latchup acts as a companion text to the author s series of books on esd electrostatic discharge protection serving as an invaluable reference for the professional semiconductor chip and system level esd engineer semiconductor device process and circuit designers and quality reliability and failure analysis engineers will find it informative on the issues that confront modern cmos technology practitioners in the automotive and aerospace industries will also find it useful in addition its academic treatment will appeal to both senior and graduate students with interests in semiconductor process device physics computer aided design and design integration

the book summarizes and compares recent advancements in the development of novel lateral power transistors ldmos devices for integrated circuits in power electronic applications in its first part the book motivates the necessity for lateral power transistors by a top down approach first it presents typical energy conversion applications in modern industrial automotive and consumer electronics next it introduces common circuit topologies suitable for these applications and discusses the feasibility for monolithic integration finally the combination of power and logic functionality on a single chip is motivated and the requirements and limitations for the power semiconductor devices are deduced the second part describes the evolution of lateral power transistors over the past decades from the simple pin type concept to double acting resurf topologies it describes the principle of operation for these ldmos devices and discusses limitations of lateral power devices moreover figures of merit are presented which can be used to evaluate the performance of the novel lateral power transistors described in this book with respect to the ldmos devices in the last part the fundamental physical concepts including charge compensation and trench gate topologies are discussed also the status of research in ldmos devices on silicon carbide is presented advantages and drawbacks for each of these integration approaches are summarized and the feasibility with respect to power electronic applications is evaluated

electrical engineering integrated circuits for wireless communications high frequency integrated circuit design is a booming

area of growth that is driven not only by the expanding capabilities of underlying circuit technologies like cmos but also by the dramatic increase in wireless communications products that depend on them integrated circuits for wireless communications includes seminal and classic papers in the field and is the first all in one resource to address this increasingly important topic internationally known and highly regarded in the field editors asad abidi paul gray and robert g meyer have meticulously compiled more than 100 papers and articles covering the very latest high level integrated circuits techniques and solutions in use today integrated circuits for wireless communications is devised expressly to provide ic design engineers system architects and integrators with a practical understanding of subjects ranging from architecture choices for integrated transceivers to actual circuit designs in all viable ic technologies such as bipolar cmos and gaas the papers selected represent a breadth of coverage and level of expertise that is simply unmatched in the field topics covered include radio architectures receivers transmitters and transceivers power amplifiers and rf switches oscillators passive components systems applications

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