

# The Asic Handbook

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written by hundreds experts who have made contributions to both enterprise and academics research these excellent reference books provide all necessary knowledge of the whole industrial chain of integrated circuits and cover topics related to the technology evolution trends fabrication applications new materials equipment economy investment and industrial developments of integrated circuits especially the coverage is broad in scope and deep enough for all kind of readers being interested in integrated circuit industry remarkable data collection update marketing evaluation enough working knowledge of integrated circuit fabrication clear and accessible category of integrated circuit products and good equipment insight explanation etc can make general readers build up a clear overview about the whole integrated circuit industry this encyclopedia is designed as a reference book for scientists and engineers actively involved in integrated circuit research and development field in addition this book provides enough guide lines and knowledges to benefit enterprisers being interested in integrated circuit industry

from asics to socs a practical approach by farzad nekoogar and faranak nekoogar covers the techniques principles and everyday realities of designing asics and socs material

includes current issues in the field front end and back end designs integration of ips on soc designs and low power design techniques and methodologies appropriate for practicing chip designers as well as graduate students in electrical engineering

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that have little relevance in a realistic design context this book concentrates on modern and evolving knowledge and design skills hardware description language hdl based design and verification is emphasized vhdl examples are used extensively throughout by treating digital logic as part of embedded systems design this book provides an understanding of the hardware needed in the analysis and design of systems comprising both hardware and software components includes a site with links to vendor tools labs and tutorials presents digital logic design as an activity in a larger systems design context features extensive use of vhdl examples to demonstrate hdl hardware description language usage at the abstract behavioural level and register transfer level as well as for low level verification and verification environments includes worked examples throughout to enhance the reader's understanding and retention of the material companion site includes links to tools for fpga design from synplicity mentor graphics and xilinx vhdl source code for all the examples in the book lecture slides laboratory projects and solutions to exercises

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fpga brings high performance applications to market quickly this book covers the many emerging platforms in a proven effective manner

madhavan swaminathan received his b.e in electronics and communication from regional engineering college tiruchirapalli in 1985 and his m.s and ph.d degrees in electrical engineering from syracuse university in 1989 and 1991 respectively he is currently the joseph m. pettit professor in electronics in the school of electrical and computer engineering and deputy director of the packaging research center georgia tech he is also the cofounder of jacket micro devices a company specializing in rf modules for wireless applications prior to joining georgia tech he was with ibm where he worked on packaging for super computers his work on power integrity has won several awards and he was made an ieee fellow for his contributions in this area ege engin received his b.s and m.s degrees in electrical engineering from middle east technical university ankara turkey and from university of paderborn germany from 2001 to 2004 he was with the fraunhofer institute for reliability and microintegration in berlin during this time he also received his ph.d from the university of hannover germany he is currently a research engineer in the school of electrical and computer engineering and an assistant research director of the packaging

research center at georgia tech he has more than 40 publications in refereed journals and conferences in the areas of signal and power integrity modeling and simulation the first comprehensive example rich guide to power integrity modeling professionals need to thoroughly understand signal and power integrity issues in order to successfully design packages and boards for high speed systems now for the first time there s a complete guide to power integrity modeling everything you need to know from the basics through the state of the art using realistic case studies and downloadable software examples two leading experts demonstrate today s best techniques for designing and modeling interconnects to efficiently distribute power and minimize noise the authors carefully introduce the core concepts of power distribution design systematically present and compare leading techniques for modeling noise and link these techniques to specific applications their many examples range from the simplest using analytical equations to compute power supply noise through complex system level applications the authors introduce power delivery network components analysis high frequency measurement and modeling requirements thoroughly explain plane modeling including plane behavior lumped modeling distributed circuit based approaches and much more offer in depth coverage of simultaneous switching noise including modeling for time and frequency domain analysis introduce three leading time domain simulation methods rational function methods signal flow graphs and mna present these and other advanced case studies high speed servers high speed differential signaling chip package analysis embedded decoupling capacitors and electromagnetic bandgap structures this book s system level focus and practical examples will make it indispensable for every professional concerned with power integrity including electrical engineers system designers signal integrity engineers and materials scientists it will also be valuable to developers building software that takes advantage of high speed systems during my m s undergraduate days in a little town called tiruchirapalli

in southern india we used to have frequent voltage and current surges that knocked out all the electrical equipment such as fans and lights in our rooms frustrated my friend once remarked we are powerless to solve the current problem of course he meant this in jest but little did i realize that this would become the theme of my research for many years though my area is on semiconductors and computer system

a guide to the converging technologies of information technology telecommunications broadcasting and multimedia production

this volume provides a comprehensive analysis of the development of corporate governances across a range of countries including australia germany spain and the uk it also gives insight into its evolution in countries with diverse cultural economic and legal systems

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