

# Synopsys Design Constraints Sdc Basics Vlsi Concepts

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functional and behavioral verification of correctness forms the bottleneck in current vlsi design systems for economical reasons design of vlsi circuits must be completely validated before manufacturing current vlsi validation is mainly done through extensive simulation the emerging alternative is based on formal design and verification methods that guarantee correctness this book describes original work in all aspects of formal hardware design methods topics covered include high level specification hardware description languages formal hardware verification methods guided synthesis methods correctness preserving transformations use of theorem provers for verification formal proof of correctness mos timing verification methods design for verifiability and practical experiences

this volume is one of four each of which consists of reprinted chapters from the highly acclaimed comprehensive two volume set intermetallic compounds principles and practice published in 1995 in some cases the author or authors have added a brief addendum to bring their chapter up to date and in other cases more recent references have been added chapters have been selected and grouped in subject areas to provide more easily accessible and user friendly volumes for individual researchers the other titles in this four volume set are crystal structures of intermetallic compounds structural applications of intermetallic compounds magnetic electrical and optical properties and applications of intermetallic compounds

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