

Synchronization Techniques For Digital Receivers Applications Of Communications Theory

Synchronization Techniques for Digital Receivers Autonomous Software-Defined Radio Receivers for Deep Space Applications Field-Programmable Logic and Applications. From FPGAs to Computing Paradigm Elements of Optical Communication and Opto Electronics The Navy Electricity and Electronics Training Series: Module 24 Introduction To Fiber Optics Direct Conversion Receivers in Wide-Band Systems 1996 Fifth IEEE International Conference on Universal Personal Communications Record High-Tc Microwave Superconductors and Applications GLOBECOM '83 Proceedings Fifth International Conference on HF Radio Systems and Techniques, 22-25 July 1991 Journal of Zhejiang University Circuits for Wireless Communications Conference Record Phase-lock-loop Application for Fiber Optic Receiver Asian Sources Electronic Components IEEE International Conference on Application-Specific Systems, Architectures and Processors Frequency Synthesis; Theory, Design & Applications Field-programmable Logic and Applications Seventh International Conference on HF Radio Systems and Techniques, 7-10 July 1997 Umberto Mengali Jon Hamkins Reiner W. Hartenstein United States. Navy Aarno Pärssinen Robert B. Hammond Banlue Srisuchinwong Lothar Thiele (Computer scientist) Věnceslav F. Kroupa

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synchronization is a critical function in digital communications its failures may have catastrophic effects on the transmission system performance furthermore synchronization circuits comprehend such a large part of the receiver hardware that their implementation has a substantial impact on the overall costs for these reasons design engineers are particularly concerned with the development of new and more efficient synchronization structures unfortunately the advent of digital vlsi technology has radically affected modem design rules to a point that most analog techniques employed so far have become totally obsolete although digital synchronization methods are well established by now in the literature they only appear in the form of technical papers often concentrating on specific performance or implementation issues as a consequence they are hardly useful to give a unified view of an otherwise seemingly heterogeneous field it is widely recognized that a fundamental understanding of digital synchronization can only be reached by providing the designer with a solid theoretical framework or else he will not know where to adjust his methods when he attempts to apply them to new situations the task of the present book is just to develop such a framework

this book introduces the reader to the concept of an autonomous software defined radio sdr receiver each distinct aspect of the design of the receiver is treated in a separate chapter written by one or more leading innovators in the field chapters begin with a problem statement and then offer a full mathematical derivation of an appropriate solution a decision metric or loop structure as appropriate and performance results

this book constitutes the refereed proceedings of the 8th international workshop on field programmable logics and applications fpl 98 held in tallinn estonia in august september 1998 the 39 revised full papers presented were carefully selected for inclusion in the book from a total of 86 submissions also included are 30 refereed high quality posters the papers are organized in topical sections on design methods general aspects prototyping and simulation development methods accelerators system architectures hardware software codesign system development algorithms on fpgas and applications

module 24 introduction to fiber optics is an introduction to fiber optics the navy electricity and electronics training series neets was developed for use by personnel in many electrical and electronic related navy ratings written by and with the advice

of senior technicians in these ratings this series provides beginners with fundamental electrical and electronic concepts through self study the presentation of this series is not oriented to any specific rating structure but is divided into modules containing related information organized into traditional paths of instruction

this book is based on my doctoral thesis at the helsinki university of technology several different projects during five years guided me from the basics of the rf ic design to the implementations of highly integrated radio receiver chips sharing time and effort between ic and system issues is not always straightforward i have been lucky to follow both topics and share experiences with diligent and enthusiastic people having different specialities as a result this book will cover a wide range of different topics needed in the design of highly integrated radio receivers experiences from the first receiver prototypes for the third generation cellular systems form the basis of this book most of the issues are directly related to the early proposals of european and japanese standardization organizations for example the chip rate was originally set to 4 096 mcps in a wide band cdma channel i have kept that number in the book in most of the examples although it has been later changed to 3 84 mcps i hope that the readers will accept that and the possible other incompatibilities to the latest specifications at least in the research phase the changes even in the most essential requirements are definitely not a rare incident and ic designers should be able to react and modify their designs as soon as they can

wireless communications is one of the fastest growing areas of communications technology this book presents current and classic papers on circuits for wireless communications selected by three experts in the field the text should be useful for students engineers just beginning to work in cellular communications and experienced engineers who want a convenient and useful reference source

these conference papers on computer hardware and design and testing include coverage of miscellaneous applications arithmetic algorithms and architectures dsp architectures systolic algorithms and architectures rapid prototyping design methodologies and compilers

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