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the 27 revised full papers presented here together with one invited paper were carefully reviewed and selected from 58 submissions the papers feature current research from the communities of verification model checking and abstract interpretation facilitating interaction cross fertilization and advancement of hybrid methods

this book constitutes the refereed proceedings of the 26th international symposium on model checking software spin 2019 held in beijing china in july 2019 the 11 full papers presented and 2 demo tool papers were carefully reviewed and selected from 29 submissions topics covered include formal verification techniques for automated analysis of software formal analysis for modeling languages such as uml state charts formal specification languages temporal logic design by contract model checking automated theorem proving including sat and smt verifying compilers abstraction and symbolic execution techniques and much more

model checking is a powerful approach for the formal verification of software when

applicable it automatically provides complete proofs of correctness or explains via counter examples why a system is not correct this book provides a basic introduction to this new technique the first part describes in simple terms the theoretical basis of model checking transition systems as a formal model of systems temporal logic as a formal language for behavioral properties and model checking algorithms the second part explains how to write rich and structured temporal logic specifications in practice while the third part surveys some of the major model checkers available

this book constitutes the refereed proceedings of the 13th international conference on verification model checking and abstract interpretation vmcai 2012 held in philadelphia pa usa in january 2012 co located with the symposium on principles of programming languages popl 2012 the 26 revised full papers presented were carefully reviewed and selected from 70 submissions the papers cover a wide range of topics including program verification model checking abstract interpretation static analysis deductive methods program certification debugging techniques abstract domains type systems and optimization

this book constitutes the thoroughly refereed post proceedings of the 5th international haifa verification conference hvc 2009 held in haifa israel in october 2009 the 11 revised full papers presented together with four abstracts of invited lectures were carefully reviewed and selected from 23 submissions the papers address all current issues challenges and future directions of verification for hardware software and hybrid systems and present academic research in the verification of systems generally divided into two paradigms formal verification and dynamic verification testing

an expanded and updated edition of a comprehensive presentation of the theory and practice of model checking a technology that automates the analysis of complex systems model checking is a verification technology that provides an algorithmic means of determining whether an abstract model representing for example a hardware or software design satisfies a formal specification expressed as a temporal logic formula if the specification is not satisfied the method identifies a counterexample execution that shows the source of the problem today many major hardware and software companies use model checking in practice for verification of vlsi circuits communication protocols software device drivers real time embedded systems and security algorithms this book offers a comprehensive presentation of the theory and practice of model checking covering the foundations of the key algorithms in depth the field of model checking has grown dramatically since the publication of the first edition in 1999 and this second edition reflects the advances in the field reorganized expanded and updated the new edition retains the focus on the foundations of temporal logic model while offering new chapters that cover topics that did not exist in 1999 propositional satisfiability sat based model checking counterexample guided abstraction refinement and software model checking the book serves as an introduction to the field suitable for classroom use and as an essential guide for researchers

a comprehensive introduction to the foundations of model checking a fully automated technique for finding flaws in hardware and software with extensive examples and both practical and theoretical exercises our growing dependence on increasingly complex computer and software systems necessitates the development of formalisms techniques and tools for assessing functional properties of these systems one such technique that has emerged in the last twenty years is model checking which systematically and automatically checks whether a model of a given system satisfies a desired property such as deadlock freedom invariants and request response properties this automated technique for verification and debugging has developed into a mature and widely used approach with

many applications principles of model checking offers a comprehensive introduction to model checking that is not only a text suitable for classroom use but also a valuable reference for researchers and practitioners in the field the book begins with the basic principles for modeling concurrent and communicating systems introduces different classes of properties including safety and liveness presents the notion of fairness and provides automata based algorithms for these properties it introduces the temporal logics Itl and ctl compares them and covers algorithms for verifying these logics discussing real time systems as well as systems subject to random phenomena separate chapters treat such efficiency improving techniques as abstraction and symbolic manipulation the book includes an extensive set of examples most of which run through several chapters and a complete set of basic results accompanied by detailed proofs each chapter concludes with a summary bibliographic notes and an extensive list of exercises of both practical and theoretical nature

model checking is a computer assisted method for the analysis of dynamical systems that can be modeled by state transition systems drawing from research traditions in mathematical logic programming languages hardware design and theoretical computer science model checking is now widely used for the verification of hardware and software in industry the editors and authors of this handbook are among the world's leading researchers in this domain and the 32 contributed chapters present a thorough view of the origin theory and application of model checking in particular the editors classify the advances in this domain and the chapters of the handbook in terms of two recurrent themes that have driven much of the research agenda the algorithmic challenge that is designing model checking algorithms that scale to real life problems and the modeling challenge that is extending the formalism beyond kripke structures and temporal logic the book will be valuable for researchers and graduate students engaged with the development of formal methods and verification tools

this book constitutes the refereed proceedings of the 9th international haifa verification conference hvc 2013 held in haifa israel in november 2013 the 24 revised full papers presented were carefully reviewed and selected from 49 submissions the papers are organized in topical sections on sat and smt based verification software testing supporting dynamic verification specification and coverage abstraction and model presentation

building information modelling bim in design construction and operations contains the proceedings of the first in a planned series of conferences dealing with design coordination construction maintenance operation and decommissioning the book gives details of how bim tools and techniques have fundamentally altered the manner in which modern construction teams operate the processes through which designs are evolved and the relationships between conceptual detail construction and life cycle stages the papers contributed by experts from industry practice and academia debate key topics develop innovative solutions and predict future trends the interdisciplinary nature of the contents and the collaborative practices discussed so important within the built environment will appeal to those engaged in design surveying visualisation infrastructure real estate construction law insurance and facilities management topics covered include bim in design coordination bim in construction operations bim in building operation and maintenance bim and sustainability bim and collaborative working and practices bim health and safety and bim facilities management integration among others

this book provides an engineering insight into how to provide a scalable and robust verification solution with ever increasing design complexity and sizes it describes sat based model checking approaches and gives engineering details on what makes model checking practical the book brings together the various sat based scalable emerging

technologies and techniques covered can be synergistically combined into a scalable solution

the refereed post proceedings of the 4th workshop on model checking and artificial intelligence are presented in this volume eight full workshop papers are presented along with three post proceedings papers papers are organized into topical sections covering planning and model checking heuristics for real time model checking verification of multi agent systems and logics for model checking and artificial intelligence

the spin workshop is a forum for researchers interested in the subject of automata based explicit state model checking technologies for the analysis and veri cation of asynchronous concurrent and distributed systems the spin del checker netlib bell labs com netlib spin whatispin html developed by gerard holzmann is one of the best known systems of this kind and has attracted a large user community this can likely be attributed to its e cient state exploration algorithms the fact that spin s modeling language promela resembles a programming language has probably also contributed to its success traditionally the spin workshops present papers on extensions and uses of spin as an experiment this year s workshop was broadened to have a slightly wider focus than previous workshops in that papers on software veri cation were encouraged consequently a small collection of papers describe attempts to analyze and verify programs written in conventional programming languages solutions include translations from source code to promela as well as specially designed model checkers that accept source code we believe that this is an teresting research direction for the formal methods community and that it will result in a new set of challenges and solutions of course abstraction becomes the key solution to deal with very large state spaces however we also see tential for integrating model checking with techniques such as static program analysis and testing papers on these issues have therefore been included in the proceedings

this book constitutes the refereed proceedings of the 10th international spin workshop on model checking of software spin 2003 held in portland or usa in may 2003 as an icse 2003 satellite workshop the 14 revised full papers and 3 revised tool papers presented were carefully reviewed and selected from 30 submissions the book presents state of the art results on the analysis and verification of distributed software systems using the spin model checker as one of the most powerful and widely applied systems

abstraction refinement for large scale model checking summarizes recent research on abstraction techniques for model checking large digital system considering both the size of today s digital systems and the capacity of state of the art verification algorithms abstraction is the only viable solution for the successful application of model checking techniques to industrial scale designs this book describes recent research developments in automatic abstraction refinement techniques the suite of algorithms presented in this book has demonstrated significant improvement over prior art some of them have already been adopted by the eda companies in their commercial in house verification tools

this festschrift volume published in celebration of the 25th anniversary of model checking features papers based on talks at the symposium 25 years of model checking 25mc which was part of the 18th international conference on computer aided verification

this book provides an engineering insight into how to provide a scalable and robust verification solution with ever increasing design complexity and sizes it describes sat based model checking approaches and gives engineering details on what makes model checking practical the book brings together the various sat based scalable emerging technologies and techniques covered can be synergistically combined into a scalable

solution

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