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madhavan swaminathan received his b e in electronics and communication from regional engineering college tiruchirapalli in 1985 and his m s and ph d degrees in electrical engineering from syracuse university in 1989 and 1991 respectively he is currently the joseph m petit professor in electronics in the school of electrical and computer engineering and deputy director of the packaging research center georgia tech he is also the cofounder of jacket micro devices a company specializing in rf modules for wireless applications prior to joining georgia tech he was with ibm where he worked on packaging for super computers his work on power integrity has won several awards and he was made an ieee fellow for his contributions in this area ege engin received his b s and m s degrees in electrical engineering from middle east technical university ankara turkey and from university of paderborn germany from 2001 to 2004 he was with the fraunhofer institute for reliability and microintegration in berlin during this time he also received his ph d from the university of hannover germany he is currently a research engineer in the school of electrical and computer engineering and an assistant research director of the packaging research center at georgia tech he has more than 40 publications in refereed journals and conferences in the areas of signal and power integrity modeling and simulation the first comprehensive example rich guide to power integrity modeling professionals need to thoroughly understand signal and power integrity issues in order to successfully design packages and boards for high speed systems now for the first time there s a complete guide to power integrity modeling everything you need to know from the basics through the state of the art using realistic case studies and downloadable software examples two leading experts demonstrate today s best techniques for designing and modeling interconnects to efficiently distribute power and minimize noise the authors carefully introduce the core concepts of power distribution design systematically present and compare leading techniques for modeling noise and link these techniques to specific applications their many examples range from the simplest using analytical equations to compute power supply noise through complex system level applications the authors introduce power delivery network components analysis high frequency measurement and modeling requirements thoroughly explain plane modeling including plane behavior lumped modeling distributed circuit based approaches and much more offer in depth coverage of simultaneous switching noise including modeling for time and frequency domain analysis introduce three leading time domain simulation methods rational function methods signal flow graphs and mna present these and other advanced case studies high speed servers high speed differential signaling chip package analysis embedded decoupling capacitors and electromagnetic bandgap structures this book s system level focus and practical examples will make it indispensable for every professional concerned with power integrity including electrical engineers system designers signal integrity engineers and materials scientists it will also be valuable to developers building software that takes advantage of high speed systems during my m s undergraduate days in a little town called tiruchirapalli in southern india we used to have frequent voltage and current surges that knocked out all the electrical equipment such as fans and lights in our rooms frustrated my friend once remarked we are powerless to solve the current problem of course he meant this in jest but little did i realize that this would become the theme of my research for many years though my area is on semiconductors and computer system

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