Computer Arithmetic Algorithms

Computer Arithmetic AlgorithmsCryptography ArithmeticAlgorithms for Computer AlgebraComputer ArithmeticInstructor's Manual For Computer ArithmeticNumerical Computation 1Computer Arithmetic SystemsJournal of Research of the National Institute of Standards and TechnologyComputer Aided VerificationAdvanced Signal-processing Algorithms, Architectures, and ImplementationsComputer Aided VerificationComputer ArithmeticAlgorithms and Design Methods for Digital Computer ArithmeticLogical and Mathematical Methods for IBM MicrocomputersMathematical Foundations of Computer ScienceApplied Algebra, Algebraic Algorithms, and Error-correcting CodesPapers Presented at ACM SIGCSE Second Symposium on Education in Computer ScienceCoordinating Mathematical and Pedagogical Content in Preservice Teacher EducationSASIMI '98ICASSP 88: D, digital signal processing Israel Koren Amos R. Omondi Keith O. Geddes Behrooz Parhami Behrooz Parhami Christoph W. Ueberhuber Amos R. Omondi Gerard Berry Mircea Vlădu□iu Behrooz Parhami Julio Sanchez Peter Shahpour Wiles

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Gerard Berry Mircea Vlădu□iu Behrooz Parhami Julio Sanchez Peter Shahpour Wiles

this text explains the fundamental principles of algorithms available for performing arithmetic operations on digital computers these include basic arithmetic operations like addition subtraction multiplication and division in fixed point and floating point number systems as well as more complex operations such as square root extraction and evaluation of exponential logarithmic and trigonometric functions the algorithms described are independent of the particular technology employed for their implementation

modern cryptosystems used in numerous applications that require secrecy or privacy electronic mail financial transactions medical record keeping government affairs social media etc are based on sophisticated mathematics and algorithms that in implementation involve much computer arithmetic and for speed it is necessary that the arithmetic be realized at the hardware chip level this book is an introduction to the implementation of cryptosystems at that level the aforementioned arithmetic is mostly the arithmetic of finite fields and the book is essentially one on the arithmetic of prime fields and binary fields in the context of cryptography the book has three main parts the first part is on generic algorithms and hardware architectures for the basic arithmetic operations addition subtraction multiplication and division the second part is on the arithmetic of prime fields and the third part is on the arithmetic of binary fields the mathematical fundamentals necessary for the latter two parts are included as are descriptions of various types of cryptosystems to provide appropriate context this book is intended for advanced level students in computer science computer engineering and electrical and electronic engineering practitioners too will find it useful as will those with a general interest in hard applications of mathematics

algorithms for computer algebra is the first comprehensive textbook to be published on the topic of computational symbolic mathematics the book first develops the foundational material from modern algebra that is required for subsequent topics it then presents a thorough development of modern computational algorithms for such problems as multivariate polynomial arithmetic and greatest common divisor calculations factorization of multivariate polynomials symbolic solution of linear and polynomial systems of equations and analytic integration of elementary functions numerous examples are integrated into the text as an aid to understanding the mathematical development the algorithms developed for

each topic are presented in a pascal like computer language an extensive set of exercises is presented at the end of each chapter algorithms for computer algebra is suitable for use as a textbook for a course on algebraic algorithms at the third year fourth year or graduate level although the mathematical development uses concepts from modern algebra the book is self contained in the sense that a one term undergraduate course introducing students to rings and fields is the only prerequisite assumed the book also serves well as a supplementary textbook for a traditional modern algebra course by presenting concrete applications to motivate the understanding of the theory of rings and fields

ideal for graduate and senior undergraduate courses in computer arithmetic and advanced digital design computer arithmetic algorithms and hardware designs second edition provides a balanced comprehensive treatment of computer arithmetic it covers topics in arithmetic unit design and circuit implementation that complement the architectural and algorithmic speedup techniques used in high performance computer architecture and parallel processing using a unified and consistent framework the text begins with number representation and proceeds through basic arithmetic operations floating point arithmetic and function evaluation methods later chapters cover broad design and implementation topics including techniques for high throughput low power fault tolerant and reconfigurable arithmetic an appendix provides a historical view of the field and speculates on its future an indispensable resource for instruction professional development and research computer arithmetic algorithms and hardware designs second edition combines broad coverage of the underlying theories of computer arithmetic with numerous examples of practical designs worked out examples and a large collection of meaningful problems this second edition includes a new chapter on reconfigurable arithmetic in order to address the fact that arithmetic functions are increasingly being implemented on field programmable gate arrays fpgas and fpga like configurable devices updated and thoroughly revised the book offers new and expanded coverage of saturating adders and multipliers truncated multipliers fused multiply add units overlapped quotient digit selection bipartite and multipartite tables reversible logic dot notation modular arithmetic montgomery modular reduction division by constants ieee floating point standard formats and interval arithmetic

this title provides a view of computer arithmetic covering topics in arithmetic unit design and circuit implementation that complement the architectural and algorithmic speedup

techniques used in high performance computer architecture and parallel processing

this book deals with various aspects of scientific numerical computing no at tempt was made to be complete or encyclopedic the successful solution of a numerical problem has many facets and consequently involves different fields of computer science computer numerics as opposed to computer algebra is thus based on applied mathematics numerical analysis and numerical computation as well as on certain areas of computer science such as computer architecture and operating systems applied mathemalies i i i numerical analysis analysis algebra i i numerical computation symbolic computation i operating systems computer hardware each chapter begins with sample situations taken from specific fields of appli cation abstract and general formulations of mathematical problems are then presented following this abstract level a general discussion about principles and methods for the numerical solution of mathematical problems is presented relevant algorithms are developed and their efficiency and the accuracy of their results is assessed it is then explained as to how they can be obtained in the form of numerical software the reader is presented with various ways of applying the general methods and principles to particular classes of problems and approaches to extracting practically useful solutions with appropriately chosen numerical software are developed potential difficulties and obstacles are examined and ways of avoiding them are discussed the volume and diversity of all the available numerical software is tremendous

aimed at digital designers computer hardware designers and computer architects this title deals with algorithms and hardware for operations in conventional fixed point number systems algorithms and hardware for operations in floating point number systems and unconventional number systems

this book constitutes the refereed proceedings of the 13th international conference on computer aided verification cav 2001 held in paris france in july 2001 the 33 revised full papers presented were carefully reviewed and selected from 106 regular paper submissions also included are 13 reviewed tool presentations selected from 27 submissions the book offers topical sections on model checking and theorem proving automata techniques verification core technology bdd and decision trees abstraction and refinement combinations infinite state systems temporal logics and verification microprocessor verification and cache coherence sat and applications and timed automata

the subject of this book is the analysis and design of digital devices that implement computer arithmetic the book s presentation of high level detail descriptions formalisms and design principles means that it can support many research activities in this field with an emphasis on bridging the gap between algorithm optimization and hardware implementation the author provides a unified view linking the domains of digital design and arithmetic algorithms based on original formalisms and hardware description languages a feature of the book is the large number of examples and the implementation details provided while the author does not avoid high level details providing for example gate level designs for all matrix combinational arithmetic structures the book is suitable for researchers and students engaged with hardware design in computer science and engineering a feature of the book is the large number of examples and the implementation details provided while the author does not avoid high level details providing for example gate level designs for all matrix combinational arithmetic structures the book is suitable for researchers and students engaged with hardware design in computer science and engineering

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logical and mathematical methods for the ibm microcomputers will teach professionals how to best understand and use the mathematical capabilities of the ibm microcomputers it is the first book to combine both logic programming and mathematical programming concepts within an understandable and useable framework the book focuses on the 8087 family of coprocessors including the 8087 80287 and the 80387 coprocessors it shows the manipulation of matrix structures in the computerized solution of linear systems develops combinatorial and brute force methods for finding heuristic solutions to mathematical problems that defy traditional analytical procedures and features coverage of the logical foundation of computer simulations and modeling including the modeling of human intelligence in neural networks discussions regarding the use of boolean algebra in the design of electronic circuits are also presented logical and mathematical methods for the ibm microcomputers is ideal for computer scientists computer engineers electrical engineers mathematicians and other scientists who use the current family of ibm coprocessors in their computers

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