

Basic Operational Amplifiers And Linear Integrated Circuits

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designed primarily for courses in operational amplifier and linear integrated circuits for electrical electronic instrumentation and computer engineering and applied science students includes detailed coverage of fabrication technology of integrated circuits basic principles of operational amplifier internal construction and applications have been discussed important linear ics such as 555 timer 565 phase locked loop linear voltage regulator ics 78 79 xx and 723 series d a and a d converters have been discussed in individual chapters each topic is covered in depth large number of solved problems review questions and experiments are given with each chapter for better understanding of text salient features of second edition additional information provided wherever necessary to improve the understanding of linear ics chapter 2 has been thoroughly revised dc ac analysis of differential amplifier has been discussed in detail the section on current mirrors has been thoroughly updated more solved examples pspice programs and answers to selected problems have been added

this accurate and easy to understand book presents readers with the basic principles of operational amplifiers and integrated circuits with a very practical approach a large number of examples questions problems and practical circuit applications make it a valuable reference guide chapter topics include an introduction to frequency response and negative feedback of op amps along with interpretation of data sheets and characteristics also covered are active filters and oscillators comparators and converters specialized ic applications and system projects for professional design engineers technologists and technicians with self study interests who need the ability to adapt to changing technology as new devices appear on the market

in this fifth edition we not only have kept the standard 741 op amp but also have shown many circuits with newer readily available op amps because these have largely overcome the dc and ac limitations of the older types we preserved or objective of simplifying the process of learning about applications involving signal conditioning signal generation filters instrumentation and control circuits but we have oriented this fifth edition to reflect the evolution of analog circuits into those applications whose purpose is to condition signals from transducers or other sources into form suitable for presentation to a microcontroller or computer in addition we have added examples of circuit simulation using pspice throughout this edition introduction

an analog chip is a set of miniature electronic analog circuits formed on a single piece of semiconductor material the voltage and current at specified points in the circuits of analog chips vary continuously in time in contrast digital chips only use and create voltages or currents at discrete levels with no intermediate values in addition to transistors analog chips often have a larger number of passive elements than digital chips typically do inductors tend to be avoided because of their large size and a transistor and capacitor together can do the work of an inductor the book broadly deals with direct and capacitor coupled opamp amplifiers frequency response and compensation to improve the performance of opamp circuits voltage and current

sources instrumentation amplifiers and precision rectifiers limiting and clamping circuits log and antilog amplifiers etc the book covers the syllabus prescribed for b e care is taken to develop the subject logically so that the book could also be used by b sc and diploma students neatly drawn diagrams stepwise illustrations and graded numerical examples are included in every chapter to support the contents

this work examines and illustrates four basic active filters 5 v digital logic ics and much more it introduces a simple procedure for designing any linear circuit and includes new material on pspice simulations

divided into two major sections this guide s coverage is current and computer simulations via spice and multisim are integrated throughout to provide experiences similar to those encountered in industry fundamentals are stressed in order to set up readers for success computer simulations are integrated as a means of verifying a by hand calculation enabling readers to perform what if experiments test the validity of differing device models or investigate second order effects

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