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r the statistical and graphical environment is rapidly emerging as an important set of teaching and research tools for biologists this book draws upon the popularity and free availability of r to couple the theory and practice of biostatistics into a single treatment so as to provide a textbook for biologists learning

statistics r or both an abridged description of biostatistical principles and analysis sequence keys are combined together with worked examples of the practical use of r into a complete practical guide to designing and analyzing real biological research topics covered include simple hypothesis testing graphing exploratory data analysis and graphical summaries regression linear multi and non linear simple and complex anova and ancova designs including nested factorial blocking spit plot and repeated measures frequency analysis and generalized linear models linear mixed effects modeling is also incorporated extensively throughout as an alternative to traditional modeling techniques the book is accompanied by a companion website wiley com go logan r with an extensive set of resources comprising all r scripts and data sets used in the book additional worked examples the biology package and other instructional materials and links

chip multiprocessors also called multi core microprocessors or cmps for short are now the only way to build high performance microprocessors for a variety of reasons large uniprocessors are no longer scaling in performance because it is only possible to extract a limited amount of parallelism from a typical instruction stream using conventional superscalar instruction issue techniques in addition one cannot simply ratchet up the clock speed on today s processors or the power dissipation will become prohibitive in all but water cooled systems compounding these problems is the simple fact that with the immense numbers of transistors available on today s microprocessor chips it is too costly to design and debug ever larger processors every year or two cmps avoid these problems by filling up a processor die with multiple relatively simpler processor cores instead of just one huge core the exact size of a cmp s cores can vary from very simple pipelines to moderately complex superscalar processors but once a core has been selected the cmp s performance can easily scale across silicon process generations simply by stamping down more copies of the hard to design high speed processor core in each successive chip generation in addition parallel code execution obtained by spreading multiple threads of execution across the various cores can achieve significantly higher performance than would be possible using only a single core while parallel threads are already common in many useful workloads there are still important workloads that are hard to divide into parallel threads the low inter processor communication latency between the cores in a cmp helps make a much wider range of applications viable candidates for parallel execution than was possible with conventional multi chip multiprocessors nevertheless limited parallelism in key applications is the main factor limiting acceptance of cmps in some types of systems after a discussion of the basic pros and cons of cmps when they are compared with conventional uniprocessors this book examines how cmps can best be designed to handle two radically different kinds of workloads that are likely to be used with a cmp highly parallel throughput sensitive applications at one end of the spectrum and less parallel latency sensitive applications at the other throughput sensitive applications such as server workloads that handle many independent transactions at once require careful balancing of all parts of a cmp that can limit throughput such as the individual cores on chip cache memory and off chip memory interfaces several studies and example systems such as the sun niagara that examine the necessary tradeoffs are presented here in contrast latency sensitive applications many desktop applications fall into this category require a focus on reducing inter core communication latency and applying techniques to help programmers divide their programs into multiple threads as easily as possible this book discusses many techniques that can be used in cmps to simplify parallel programming with an emphasis on research directions proposed at stanford university to illustrate the advantages possible with a cmp using a couple of solid examples extra focus

is given to thread level speculation tls a way to automatically break up nominally sequential applications into parallel threads on a cmp and transactional memory this model can greatly simplify manual parallel programming by using hardware instead of conventional software locks to enforce atomic code execution of blocks of instructions a technique that makes parallel coding much less error prone contents the case for cmps improving throughput improving latency automatically improving latency using manual parallel programming a multicore world the future of cmps

twenty one articles from the 31st lsrl investigate cutting edge issues and interfaces across phonology pragmatics sociolinguistics semantics and syntax in multiple dialects of such romance languages as catalan french creole french and spanish both old and modern research in romance phonology moves from the quantitative and synchronic to cover issues of diachrony and optimality theory work within pragmatics and sociolinguistics also explores the synchronic diachronic link while topicalizing such issues as change of non pro drop swiss french toward pro drop status scalar implicatures speech acts word order and simplification in contexts of language contact finally debates in linguistic theory are resumed in the work on syntax and semantics within both a minimalist perspective and an optimality framework how do catalan and french children acquire agr and tns can basque spanish be compared to topic oriented chinese if spanish preverbal subjects occur in an a position can spanish no longer be compared to greek

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