

Ramp Phet Simulation Lab Answers

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this book covers algorithmic aspects of computer aided circuit design for vlsi of large circuits the large scale aspect of vlsi requires a reorientation towards new and more

efficient techniques many algorithms have survived the test of time while others are suffering from the usual problem of polynominal or exponential running time complexity and storage requirements the approaches presented in this book are techniques which were developed in response to the vlsi problems the most recent exact circuit analysis and simulation techniques are presented such as waveform relaxation and timing simulation the book concentrates on the analysis and simulation of large circuits which exceed the capabilities of general purpose analyzers in both compute time and storage also discussed are circuit models for switch level simulation techniques and circuit models for interconnections capacitance and inductances and optimization techniques the language and notation have been kept uniform throughout the book to help the reader to maintain the continuity between the topics discussed in the different chapters all algorithms are written in a pascal style the terminology used should reflect the emerging language used in most of the vlsi circuit design community the book includes proven approaches as well as techniques which are presently in a research state

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