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this book is concerned with advances in serial data computational architectures and the cad tools for their implementation in silicon the bit serial tradition at edinburgh university eu stretches back some 6 years to the conception of the first silicon compiler first owes much of its inspiration to dick lyon then at xerox p arc who proposed a structured design methodology for construction of signal processing systems from bit serial building blocks based on an nmos cell library first automates much of lyon s physical design process more recently we began to feel that first should be able to exploit more modern technologies before this could be achieved we were faced with a massive manual re design task i e the porting of first cell library to a new technology as it was to avoid such tasks that first was conceived in the first place we decided to move the level of user specification much nearer to the silicon level while still hiding details of transistor circuit design place and route etc from the user and by so doing enable the specification of more functionally powerful libraries in technology free form the results of this work are in evidence as advances in serial data design techniques and the second silicon compiler introduced later in this book these achievements could not have been accomplished

without help from various sources we take this opportunity to thank profs

digital signal processing dsp is used in a wide range of applications such as speech telephone mobile radio video radar and sonar the sample rate requirements of these applications range from 10 khz to 100 mhz real time implementation of these systems requires design of hardware which can process signal samples as these are received from the source as opposed to storing them in buffers and processing them in batch mode efficient implementation of real time hardware for dsp applications requires study of families of architectures and implementation styles out of which an appropriate architecture can be selected for a specified application to this end the digit serial implementation style is proposed as an appropriate design methodology for cases where bit serial systems cannot meet the sample rate requirements and bit parallel systems require excessive hardware the number of bits processed in a clock cycle is referred to as the digit size the hardware complexity and the achievable sample rate increase with increase in the digit size as special cases a digit serial system is reduced to bit serial or bit parallel when the digit size is selected to equal one or the word length respectively a family of implementations can be obtained by changing the digit size parameter thus permitting an optimal trade off between throughput and size because of their structured architecture digit serial designs lend themselves to automatic compilation from algorithmic descriptions an implementation of this design methodology the parsifal silicon compiler was developed at the general electric corporate research and development laboratory

digital filters and signal processing third edition with matlab exercises presents a general survey of digital signal processing concepts design methods and implementation considerations with an emphasis on digital filters it is suitable as a textbook for senior undergraduate or first year

graduate courses in digital signal processing while mathematically rigorous the book stresses an intuitive understanding of digital filters and signal processing systems with numerous realistic and relevant examples hence practicing engineers and scientists will also find the book to be a most useful reference the third edition contains a substantial amount of new material including in particular the addition of matlab exercises to deepen the students understanding of basic dsp principles and increase their proficiency in the application of these principles the use of the exercises is not mandatory but is highly recommended other new features include normalized frequency utilized in the dtft e g $x[n]e^{j\omega n}$ new computer generated drawings and matlab plots throughout the book chapter 6 on sampling the dtft has been completely rewritten expanded coverage of types i iv linear phase fir filters new material on power and doubly complementary filters new section on quadrature mirror filters and their application in filter banks new section on the design of maximally flat fir filters new section on roundoff noise reduction using error feedback and many new problems added throughout

modern cryptosystems used in numerous applications that require secrecy or privacy electronic mail financial transactions medical record keeping government affairs social media etc are based on sophisticated mathematics and algorithms that in implementation involve much computer arithmetic and for speed it is necessary that the arithmetic be realized at the hardware chip level this book is an introduction to the implementation of cryptosystems at that level the aforementioned arithmetic is mostly the arithmetic of finite fields and the book is essentially one on the arithmetic of prime fields and binary fields in the context of cryptography the book has three main parts the first part is on generic algorithms and hardware architectures for the basic arithmetic operations addition subtraction multiplication and division the second part is on

the arithmetic of prime fields and the third part is on the arithmetic of binary fields the mathematical fundamentals necessary for the latter two parts are included as are descriptions of various types of cryptosystems to provide appropriate context this book is intended for advanced level students in computer science computer engineering and electrical and electronic engineering practitioners too will find it useful as will those with a general interest in hard applications of mathematics

this book covers the further advances in the field of the internet of things biomedical engineering and cyber physical system with recent applications it is covering the various real time offline applications and case studies in the field of recent technologies and case studies of the internet of things biomedical engineering and cyber physical system with recent technology trends in the twenty first century the automation and management of data are vital in that the role of the internet of things proving the potential support the book is consisting the excellent work of researchers and academicians who are working in the domain of emerging technologies e g internet of things biomedical engineering and cyber physical system the chapters cover the major achievements by solving and suggesting many unsolved problems which are sure to be going to prove a strong support in industries towards automation goal using of the internet of things biomedical engineering and cyber physical system

when half of the guests at a charity masquerade fete at drool court turn up dressed as sheiks it must be more than pure coincidence one of them is the real thing however and sir john appleby master detective discovers that he is in grave danger

this book constitutes the refereed proceedings of the 13th international haifa verification

conference hvc 2017 held in haifa israel in november 2017 the 13 revised full papers presented together with 4 poster and 5 tool demo papers were carefully reviewed and selected from 45 submissions they are dedicated to advance the state of the art and state of the practice in verification and testing and are discussing future directions of testing and verification for hardware software and complex hybrid systems

vhdl the ieee standard hardware description language for describing digital electronic systems has recently been revised the designer s guide to vhdl has become a standard in the industry for learning the features of vhdl and using it to verify hardware designs this third edition is the first comprehensive book on the market to address the new features of vhdl 2008 first comprehensive book on vhdl to incorporate all new features of vhdl 2008 the latest release of the vhdl standard helps readers get up to speed quickly with new features of the new standard presents a structured guide to the modeling facilities offered by vhdl shows how vhdl functions to help design digital systems includes extensive case studies and source code used to develop testbenches and case study examples helps readers gain maximum facility with vhdl for design of digital systems

fundamentals of digital logic with vhdl design teaches the basic design techniques for logic circuits the text provides a clear and easily understandable discussion of logic circuit design without the use of unnecessary formalism it emphasizes the synthesis of circuits and explains how circuits are implemented in real chips fundamental concepts are illustrated by using small examples which are easy to understand then a modular approach is used to show how larger circuits are designed vhdl is a complex language so it is introduced gradually in the book each vhdl feature is presented as it becomes pertinent for the circuits being discussed while it

includes a discussion of vhdl the book provides thorough coverage of the fundamental concepts of logic circuit design independent of the use of vhdl and cad tools a cd rom containing all of the vhdl design examples used in the book as well altera s quartus ii cad software is included free with every text

this two volume set constitutes the post conference proceedings of the 5th eai international conference on advanced hybrid information processing adhip 2021 held in october 2021 due to covid 19 the conference was held virtually the 94 papers presented were selected from 254 submissions and focus on theory and application of hybrid information processing technology for smarter and more effective research and application the theme of adhip 2020 was social hybrid data processing the papers are named in topical sections as follows intelligent algorithms in complex environment ai system research and model design method research on internet of things technology research and analysis with intelligent education

white dwarfs each containing about as much mass as our sun but packed into a volume about the size of earth are the endpoints of evolution for most stars thousands of these faint objects have now been discovered though only a century ago only three were known they are among the most common stars in the milky way galaxy and they have become important tools in understanding the universe yet a century ago only three white dwarfs were known the existence of these stars completely baffled the scientists of the day and solving the mysteries of these strange objects required revolutionary advances in science and technology including the development of quantum physics the construction and utilization of large telescopes the invention of the digital computer and the ability to make astronomical observations from space this book tells the story of the growth in our understanding of white dwarf stars set within the

context of the relevant scientific and technological advances part popular science part historical narrative this book is authored by one of the astrophysicists who participated directly in uncovering some of the secrets of white dwarf stars

when did i stop seeing her as my best friend ever since the day we met i ve thought of her as a little sister she was a girl that needed my protection but something changed she grew up and now when i look at her i want to kiss her and make her mine but i can t she s my friend and more than that she s my bodyguard i do not mix business and pleasure

this book comprises select peer reviewed proceedings of the international conference on research in intelligent and computing in engineering rice 2020 held at thu dau mot university vietnam the volume primarily focuses on latest research and advances in various computing models such as centralized distributed cluster grid and cloud computing practical examples and real life applications of wireless sensor networks mobile ad hoc networks and internet of things data mining and machine learning are also covered in the book the contents aim to enable researchers and professionals to tackle the rapidly growing needs of network applications and the various complexities associated with them

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