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the topic areas presented within this volume focus on design environments and the applications of hardware description and modelling including simulation verification by correctness proofs synthesis and test the strong relationship between the topics of chdl 91 and the work around the use and re standardization of the vhdl language is also explored the quality of this proceedings and its significance to the academic and professional worlds is assured by the excellent technical programme here compiled

the second half of this century will remain as the era of proliferation of electronic computers they did exist before but they were mechanical during next century they may perform other mutations to become optical or molecular or even biological actually all these aspects are only fancy dresses put on mathematical machines this was always recognized to be true in the domain of software where machine or high level languages are more or less rigourous but immaterial variations of the universaly accepted mathematical language aimed at specifying elementary operations functions algorithms and processes but even a mathematical machine needs a physical support and this is what hardware is all about the invention of hardware description languages hdl s in the early 60 s was an attempt to stay longer at an abstract level in the design process and to push the stage of physical implementation up to the moment when no more technology independant decisions can be taken it was also an answer to the continuous exponential growth of complexity of systems to be designed this problem is common to hardware and software and may explain why the syntax of hardware description languages has followed with a reasonable delay of ten years the evolution of the programming languages at the end of the 60 s they were algol like a decade later pascal like and now they are c or ada like they have also integrated the new concepts of advanced software specification languages

hardware description languages hdls have established themselves as one of the principal means of designing electronic systems the interest in and usage of hdls continues to spread rapidly driven by the increasing complexity of systems the growth of hdl driven synthesis the research on formal design methods and many other related advances this research oriented publication aims to make a strong contribution to further developments in the field the following topics are explored in depth bdd based system design and analysis system level formal verification formal reasoning on hardware languages for protocol specification vhdl hdl based

design methods high level synthesis and text graphical hdls there are short papers covering advanced design capture and recent work in high level synthesis and formal verification in addition several invited presentations on key issues discuss and summarize recent advances in real time system design automatic verification of sequential circuits and languages for protocol specification

in the past few decades computer hardware description languages chdls have been a rapidly expanding subject area due to a number of factors including the advancing complexity of digital electronics the increasing prevalence of generic and programmable components of software hardware and the migration of vlsi design to high level synthesis based on hdls currently the subject has reached the consolidation phase in which languages and standards are being increasingly used at the same time as the scope is being broadened to additional application areas this book presents the latest developments in this area and provides a forum from which readers can learn from the past and look forward to what the future holds

hardware description language a complete guide

languages for precisely describing the behavior of computers have been studied since the conception of the computer itself as the computer industry has grown so has the need to exchange information about computers computer hardware description languages chdl are required to provide specifications and detailed implementation for a growing menu of sophisticated design automation tools including synthesis verification simulation analysis optimization placement wiring and testing communicate requirements and capabilities between suppliers and users of computer components and subsystems facilitate the transfer of new methods and results within the university and industrial research community as reflected in this book there is an increased emphasis on applications and resulting requirements that are placed on chdls in the field of synthesis a major application area there is today a focus on high level synthesis and synthesis under design constraints in the area of design correctness research has shifted away from simulation to formal verification techniques such as temporal logic

xv from the old to the new xvii acknowledgments xxi 1 verilog a tutorial introduction 1 getting started 2 a structural description 2 simulating the binarytoeseg driver 4 creating ports for the module 7 creating a testbench for a module 8 11 behavioral modeling of combinational circuits procedural models 12 rules for synthesizing combinational circuits 13 14 procedural modeling of clocked sequential circuits modeling finite state machines 15 rules for synthesizing sequential systems 18 non blocking assignment

hardware description languages hdl such as vhdl and verilog have found their way

into almost every aspect of the design of digital hardware systems since their inception they gradually proved to be an essential part of modern design methodologies and design automation tools ever exceeding their original goals of being description and simulation languages their use for automatic synthesis formal proof and testing are good examples so far hdls have been mainly dealing with digital systems however integrated systems designed today require more and more analog parts such as a d and d a converters phase locked loops current mirrors etc the verification of the complete system therefore asks for the use of a single language using vhdl or verilog to handle analog descriptions is possible as it is shown in this book but the real power is coming from true mixed signal hdls that integrate discrete and continuous semantics into a unified framework analog hdls ahdl are considered here a subset of mixed signal hdls as they intend to provide the same level of features as hdls do but with a scope limited to analog systems possibly with limited support of discrete semantics analog and mixed signal hardware description languages covers several aspects related to analog and mixed signal hardware description languages including the use of a digital hdl for the description and the simulation of analog systems the emergence of extensions of existing standard hdls that provide true analog and mixed signal hdls the use of analog and mixed signal hdls for the development of behavioral models of analog electronic building blocks operational amplifier pll and for the design of microsystems that do not only involve electronic parts the use of a front end tool that eases the description task with the help of a graphical paradigm yet generating and descriptions automatically analog and mixed signal hardware description languages is the first book to show how to use these new hardware description languages in the design of electronic components and systems it is necessary reading for researchers and designers working in electronic design

the symposium on which this book is based has become established as the focal point for the meeting of experts in the field of formal descriptions of hardware and their use in analysis and synthesis of digital systems the papers reflect the gradual shift from the original emphasis on the uses of language design to describe hardware toward more formal techniques for specification and verification

hardbound the papers of this seventh conference reflect the gradual shift from the original emphasis on the uses of language design to describe hardware toward more formal techniques for specification and verification this volume highlights the following topics languages to specify and describe hardware design to reason about timing and functional behaviour and to support modelling and performance evaluation synthesis and verification of systems as means of support for the design process and as a guarantee of design consistency and functional correctness tool integration aspects such as the representation of design

information and the putting together of tools within a coherent design environment

languages for precisely describing the behavior of computers have been studied since the conception of the computer itself as the computer industry has grown so has the need to exchange information about computers computer hardware description languages chdl are required to provide specifications and detailed implementation for a growing menu of sophisticated design automation tools including synthesis verification simulation analysis optimization placement wiring and testing communicate requirements and capabilities between suppliers and users of computer components and subsystems facilitate the transfer of new methods and results within the university and industrial research community as reflected in this book there is an increased emphasis on applications and resulting requirements that are placed on chdls in the field of synthesis a major application area there is today a focus on high level synthesis and synthesis under design constraints in the area of design correctness research has shifted away from simulation to formal verification techniques such as temporal logic

this rigorous text shows electronics designers and students how to deploy verilog in sophisticated digital systems design the second edition is completely updated along with the many worked examples for verilog 2001 new synthesis standards and coverage of the new ovi verification library

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