

# Mosfet Modeling For Vlsi Simulation Theory And Practice

MOSFET Models for VLSI Circuit Simulation Mosfet Modeling for VLSI Simulation Simulation in Research and Development Circuit Theory and Design Catalog Digital Technical Journal of Digital Equipment Corporation IEEE International Conference on Electronics, Circuits and Systems Modeling and Simulation Proceedings PSEUDO-BOOLEAN LOGIC CIRCUITS The Summary of Engineering Research IEEE International Symposium on Circuits and Systems Journal of KIEE Bell Laboratories Talks and Papers Report CS-R Research Needs in Dynamic Systems and Control: Machine dynamics Peterson's Graduate Programs in Engineering and Applied Sciences, 1996 1994 IEEE International Symposium on Circuits and Systems BioMEMS and Nanotechnology Choice Narain D. Arora Narain Arora International Association for Mathematics and Computers in Simulation R. Boite University of Colorado Boulder John P. Hayes University of Illinois at Urbana-Champaign. Office of Engineering Publications Bell Telephone Laboratories. Libraries and Information Systems Center Herbert H. Richardson Peterson's Guides MOSFET Models for VLSI Circuit Simulation Mosfet Modeling for VLSI Simulation Simulation in Research and Development Circuit Theory and Design Catalog Digital Technical Journal of Digital Equipment Corporation IEEE International Conference on Electronics, Circuits and Systems Modeling and Simulation Proceedings PSEUDO-BOOLEAN LOGIC CIRCUITS The Summary of Engineering Research IEEE International Symposium on Circuits and Systems Journal of KIEE Bell Laboratories Talks and Papers Report CS-R Research Needs in Dynamic Systems and Control: Machine dynamics Peterson's Graduate Programs in Engineering and Applied Sciences, 1996 1994 IEEE International Symposium on Circuits and Systems BioMEMS and Nanotechnology Choice *Narain D. Arora Narain Arora International Association for Mathematics and Computers in Simulation R. Boite University of Colorado Boulder John P. Hayes University of Illinois at Urbana-Champaign. Office of Engineering Publications Bell Telephone Laboratories. Libraries and Information Systems Center Herbert H. Richardson Peterson's Guides*

metal oxide semiconductor mos transistors are the basic building block of mos integrated circuits. In very large scale integrated vlsi circuits using mos technology have emerged as the dominant technology in the semiconductor industry over the past decade the complexity of mos ic's has increased at an astonishing rate. This is realized mainly through the reduction of mos transistor dimensions. In addition to the improvements in processing today vlsi circuits with over 3 million transistors on a chip with effective or electrical channel lengths of 0.5 microns are in volume production. Designing such complex chips is virtually impossible without simulation tools which help to predict circuit behavior before actual circuits are fabricated. However the utility of simulators as a tool for the design and analysis of circuits depends on the adequacy of the device models used in the simulator. This problem is further aggravated by the technology trend towards smaller and smaller device dimensions which increases the complexity of the models. There is extensive literature available on modeling these short channel devices. However there is a lot of confusion too often it is not clear what model to use and which model parameter values are

important and how to determine them after working over 15 years in the field of semiconductor device modeling i have felt the need for a book which can fill the gap between the theory and the practice of mos transistor modeling this book is an attempt in that direction

a reprint of the classic text this book popularized compact modeling of electronic and semiconductor devices and components for college and graduate school classrooms and manufacturing engineering over a decade ago the first comprehensive book on mos transistor compact modeling it was the most cited among similar books in the area and remains the most frequently cited today the coverage is device physics based and continues to be relevant to the latest advances in mos transistor modeling this is also the only book that discusses in detail how to measure device model parameters required for circuit simulations the book deals with the mos field effect transistor mosfet models that are derived from basic semiconductor theory various models are developed ranging from simple to more sophisticated models that take into account new physical effects observed in submicron transistors used in today s 1993 mos vlsi technology the assumptions used to arrive at the models are emphasized so that the accuracy of the models in describing the device characteristics are clearly understood due to the importance of designing reliable circuits device reliability models are also covered understanding these models is essential when designing circuits for state of the art mos ics

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