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spectral techniques and fault detection focuses on the spectral techniques for the analysis testing and design of digital devices this book discusses the error detection and correction in digital devices organized into 10 chapters this book starts with an overview of the concepts and tools to evaluate the applicability of various spectral approaches and fault detection techniques to the design this text then describes the class of generalized programmable logic array configurations called encoded plas other chapters consider the two sided chrestenson transform to the analysis of some pattern properties this book describes as well a certain type of cellular arrays for highly parallel processing namely three dimensional arrays the final chapter deals with the system design methods that allow and encourage designers to incorporate the necessary distributed error correction throughout any digital system this book is a valuable resource for graduate students and engineers working in the fields of logic design spectral techniques testing and self testing of digital devices

test and design for testability in mixed signal integrated circuits deals with test and design for test of analog and mixed signal integrated circuits especially in system on chip soc where different technologies are intertwined analog digital sensors rf test is becoming a true bottleneck of present and future ic projects linking design and test in these heterogeneous systems will have a tremendous impact in terms of test time cost and proficiency although it is recognized as a key issue for developing complex ics there is still a lack of structured references presenting the major topics in this area the aim of this book is to present basic concepts and new ideas in a manner understandable for both professionals and students since this is an active research field a comprehensive state of the art overview is very valuable introducing the main problems as well as the ways of solution that seem promising emphasizing their basis strengths and weaknesses in essence several topics are presented in detail first of all techniques for the efficient use of dsp based test and cad test tools standardization is another topic considered in the book with focus on the ieee 1149 4 also addressed in depth is the connecting design and test by means of using high level behavioural description techniques specific examples are given another issue is related to test techniques for well defined classes of integrated blocks like data converters and phase locked loops besides these specification driven testing techniques fault driven approaches are described as they offer potential solutions which are more similar to digital test methods finally in design for testability and built in self test two other concepts that were taken from digital design are introduced in an analog context and illustrated for the case of integrated filters in summary the purpose of this book is to provide a glimpse on recent research results in the area of testing mixed signal integrated circuits specifically in the topics mentioned above much of the work reported herein has been performed within cooperative european research projects in which the authors of the different chapters have actively

collaborated it is a representative snapshot of the current state of the art in this emergent field

an easy to use introduction to the practices and techniques in the field of digital circuit testing lala writes in a user friendly and tutorial style making the book easy to read even for the newcomer to fault tolerant system design each informative chapter is self contained with little or no previous knowledge of a topic assumed extensive references follow each chapter

this book contains the papers presented at the 13th international workshop on field programmable logic and applications fpl held on september 1 3 2003 the conference was hosted by the institute for systems and computer engineering research and development of lisbon inesc id and the depa ment of electrical and computer engineering of the ist technical university of lisbon portugal the fpl series of conferences was founded in 1991 at oxford university uk and has been held annually since in oxford 3 times vienna prague darmstadt london tallinn glasgow villach belfastandmontpellier itbrings together academic researchers industrial experts users and newcomers in an formal welcomingatmospherethatencouragesproductiveexchangeofideasand knowledge between delegates exciting advances in eld programmable logic show no sign of slowing down new grounds have been broken in architectures design techniques run time con guration and applications of eld programmable devices in several di erent areas many of these innovations are reported in this volume the size of fpl conferences has grown signi cantly over the years fpl in 2002 saw 214 papers submitted representing an increase of 83 when compared to the year before the interest and support for fpl in the programmable logic community continued this year with 216 papers submitted the technical p gram was assembled from 90 selected regular papers and 56 posters resulting in this volume of proceedings the program also included three invited plenary keynote presentations from lsi logic xilinx and cadence and three industrial tutorials from altera mentor graphics and dafca

device testing represents the single largest manufacturing expense in the semiconductor industry costing over 40 billion a year the most comprehensive and wide ranging book of its kind testing of digital systems covers everything you need to know about this vitally important subject starting right from the basics the authors take the reader through automatic test pattern generation design for testability and built in self test of digital circuits before moving on to more advanced topics such as iddq testing functional testing delay fault testing memory testing and fault diagnosis the book includes detailed treatment of the latest techniques including test generation for various fault models discussion of testing techniques at different levels of integrated circuit hierarchy and a chapter on system on a chip test synthesis written for students and engineers

it is both an excellent senior graduate level textbook and a valuable reference

test functions fault detection diagnosis error correction repair etc that are applied concurrently while the system continues its intended function are defined as on line testing in its expanded scope on line testing includes the design of concurrent error checking subsystems that can be themselves self checking fail safe systems that continue to function correctly even after an error occurs reliability monitoring and self test and fault tolerant designs on line testing for vlsi contains a selected set of articles that discuss many of the modern aspects of on line testing as faced today the contributions are largely derived from recent ieee international on line testing workshops guest editors michael nicolaidis yervant zorian and dhiraj pradhan organized the articles into six chapters in the first chapter the editors introduce a large number of approaches with an expanded bibliography in which some references date back to the sixties on line testing for vlsi is an edited volume of original research comprising invited contributions by leading researchers

the continual explosion of computer development has led to inadequate coverage of proper useful on line testing techniques this text fills the gap in the literature by presenting the latest techniques available for digital devices used in the most popular computers initial chapters explore the classic problems of on line testing pointing out the limited applications of conventional approaches to the problem of diagnosing digital devices using lsi vlsi chips chapters 4 7 cover compact testing methods used to diagnose complex digital circuits chapters 8 9 analyze the techniques of compressing output responses of a digital circuit while chapter 10 surveys promising recent signature generation techniques for binary sequences the final chapter covers multi output digital circuits

the first comprehensive description of systematic methods for designing optional error detection circuits table of contents overview and introduction combinatorial error detection circuitry sequential error detection circuits design algorithms for error detection circuits appendix references index 100 illustrations

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