Computer Architecture And Organization Miles Murdocca

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an accessible introduction to computer systems and architecture anyone aspiring to more advanced studies in computer related fields must gain an

understanding of the two parallel aspects of the modern digital computer programming methodology and the underlying machine architecture the uniquely

integrated approach of computer architecture and organization connects the programmer s view of a computer system with the associated hardware and

peripheral devices providing a thorough three dimensional view of what goes on inside the machine covering all the major topics normally found in a first course

in computer architecture the text focuses on the essentials including the instruction set architecture isa network related issues and programming methodology

using real world case studies to put the information into perspective the chapters examine data representation arithmetic the instruction set architecture datapath

and control languages and the machine memory buses and peripherals networking and communication advanced computer architecture a valuable feature of

this book is the use of arc a subset of the sparc processor for an instruction set architecture a platform independent arctools suite containing an assembler and

simulator for the arc isa that supports the examples used in the book is available better yet the content is supplemented by online problem sets available

through wileyplus balanced and thoughtfully designed for use as either a classroom text or self study guide computer architecture and organization an integrated

approach will put you solidly on track for advancing to higher levels in computer related disciplines about the author miles murdoccaserves as the president and

ceo of internet institute usa iiusa a private postsecondary information technology it school specializing in networking operating systems ip telephony

programming and security previously dr murdocca has been a computer science faculty member at rutgers university and a research scientist at at t bell

laboratories working in computer architecture networking and digital optical computing he is the author of a digital design methodology for optical computing and

principles of computer architecture and a contributing author to computer systems design and architecture second edition as well as the author of dozens of

professional papers and patents relating to information technology vince heuring is an associate professor and acting chair of the department of electrical and

computer engineering at the university of colorado at boulder he has been at the university since 1984 and prior to that he spent three years at the university of

cincinnati professor heuring s research encompasses computer architectures and programming language design implementation he and his colleague harry

jordan designed and built the world s first stored program optical computer spoc

this piece covers computer architecture at the instruction set architecture isa and system design levels starting with foundation material on data representation and computer arithmetic the book moves through the basic components of a computer architecture covering topics at increasing levels of complexity up through cisc network architecture and parallel architecture the authors have adopted the use of a sparc subset for an instructional isa called arc a risc computer which is carried through the mainstream of the book and is complemented with platform independent software tools that simulate the arc isa as well as the mips and x86 pentium isas features benefits choice of the instruction set architecture isa the mainstream isa arc is a subset of the commercial sparc which strikes a balance between the complexity of a real world architecture and the need for a simple instructional isa companion website prenhall com murdocca software available on companion website assembles and simulates program execution on sparc subset arc mips and intel isas simulators and assemblers run an pcs macs and unix over 400 adobe acrobat slides simplify lecture preparation password protected area of companion website case studies over 200 homework problems the major portion of the text deals with a high level look at computer architecture while the appendices and case studies cover lower level technology dependent aspects allows computer architecture to be studied at all levels

the 1987 princeton workshop on algorithm architecture and technology issues for models of concurrent computation was organized as an interdisciplinary work shop emphasizing current research directions toward concurrent computing systems with participants from several different fields of specialization the workshop cov ered a wide variety of topics though by no means a complete cross section of issues in this rapidly moving field the papers included in this book were prepared for the workshop and taken together provide a view of the broad range of issues and alternative directions being explored to organize the various papers the book has been divided into five parts part i considers new technology directions part ii emphasizes underlying theoretical issues communication issues which are ad dressed in the majority of papers are specifically highlighted in part iii part iv includes papers stressing the fault tolerance and reliability of

systems finally part v includes systems oriented papers where the system ranges from vlsi circuits through powerful parallel computers much of the initial planning of the workshop was completed through an informal at t bell laboratories group consisting of mehdi hatamian vijay kumar adri aan ligtenberg sailesh rao p subrahmanyam and myself we are grateful to stuart schwartz both for the support of princeton university and for his orga nizing local arrangements for the workshop and to the members of the organizing committee whose recommendations for participants and discussion topics were par ticularly helpful a rosenberg and a t

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this book presents a valuable new methodology for simplifying the design of digital circuits for systems that use optics as an interconnection medium

giving introduction to computer systems and architecture this book covers the major topics normally found in a first course in computer architecture it focuses on the essentials including the instruction set architecture network related issues and programming methodology it uses real world case studies to put the information into perspective

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